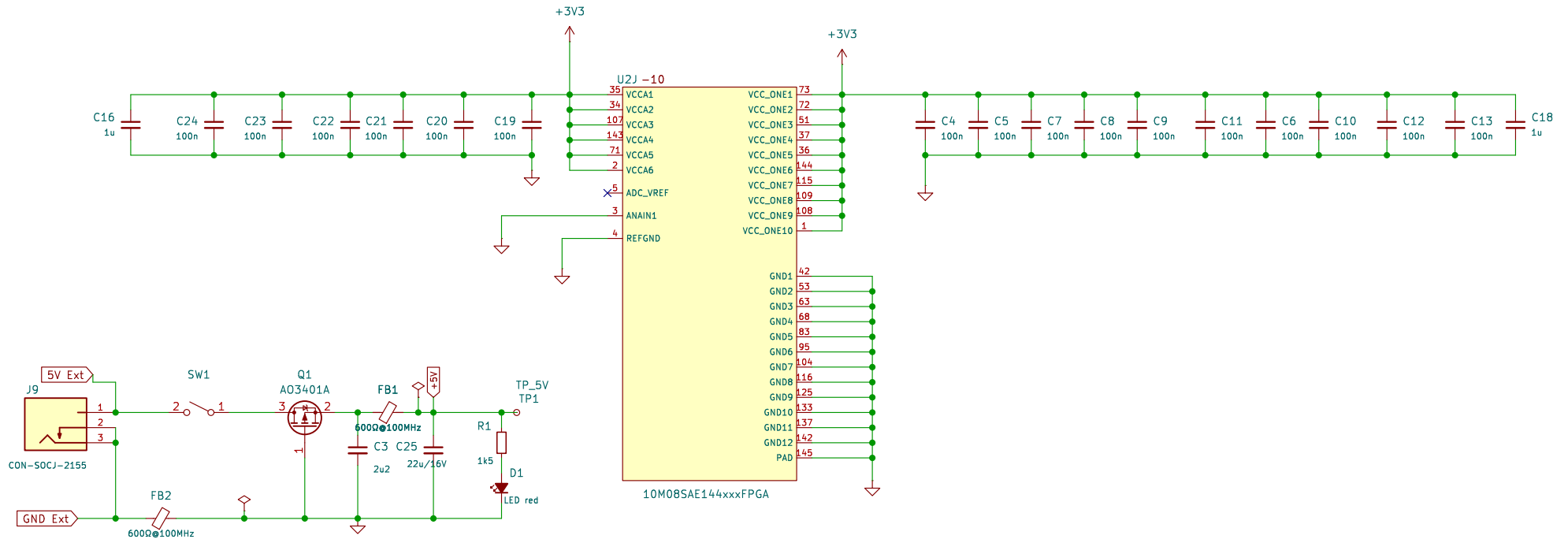
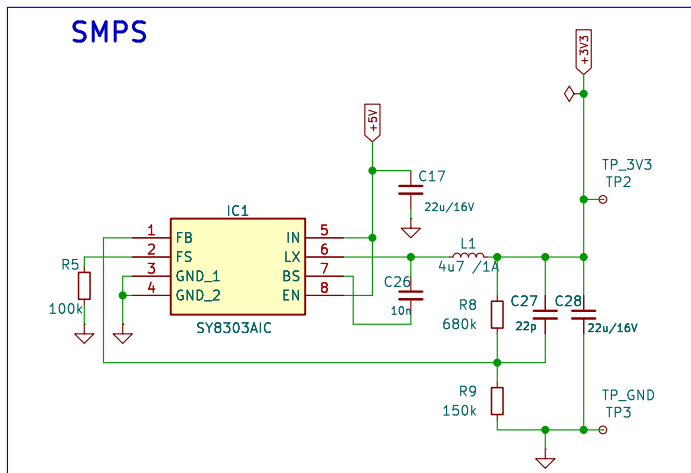


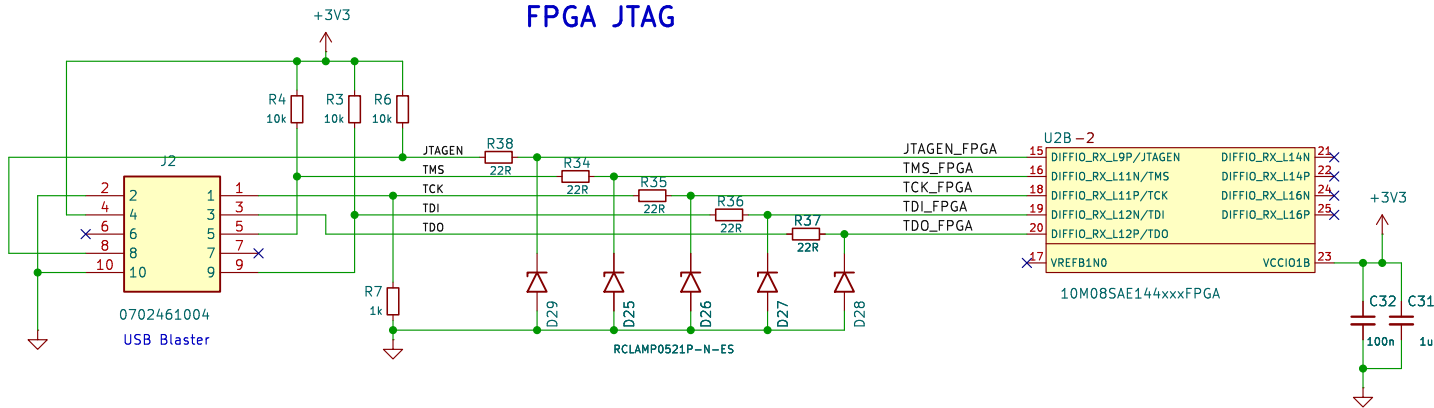
FPGA Power Supply



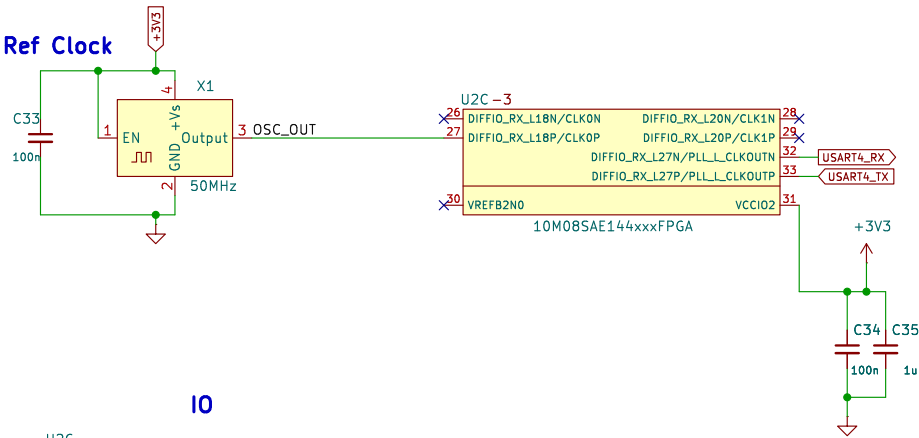
SMPS



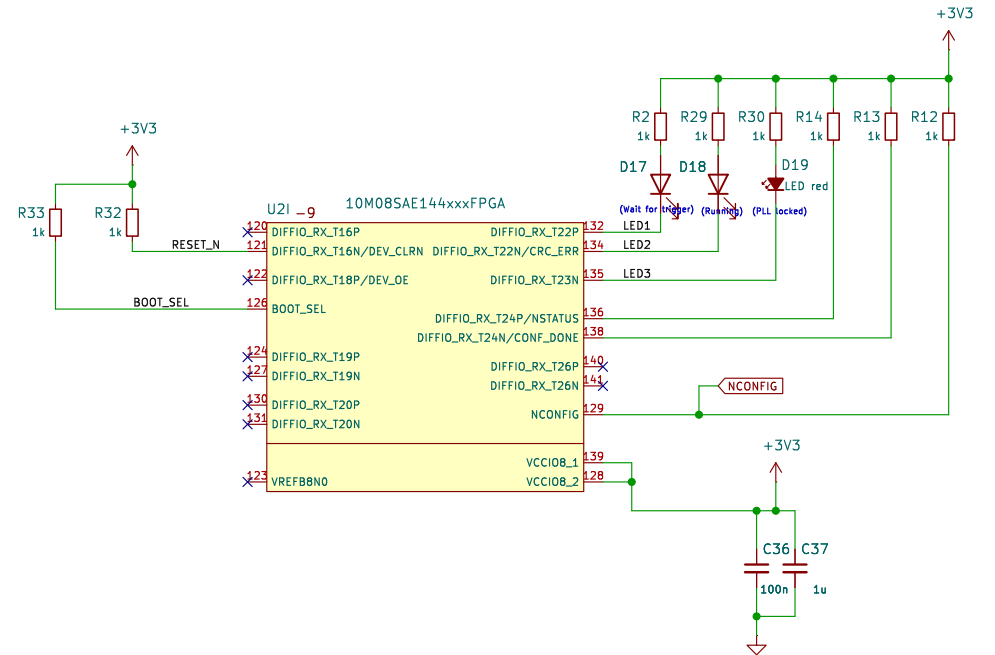
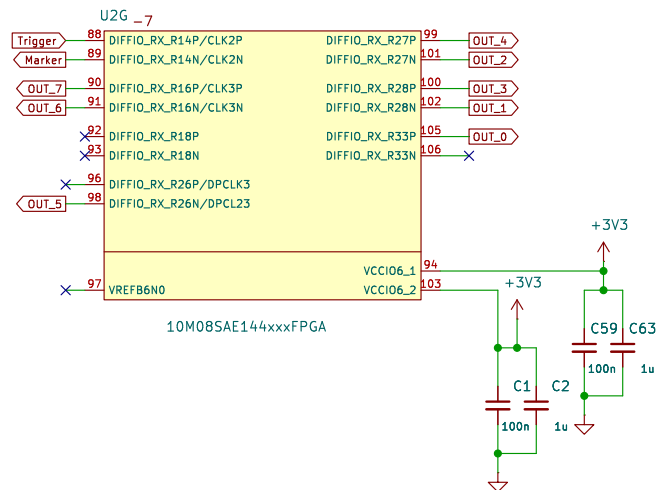
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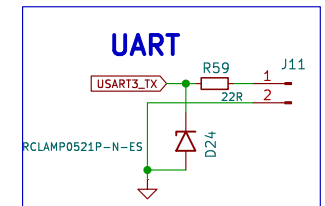
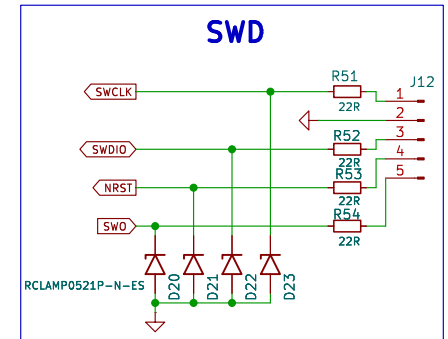
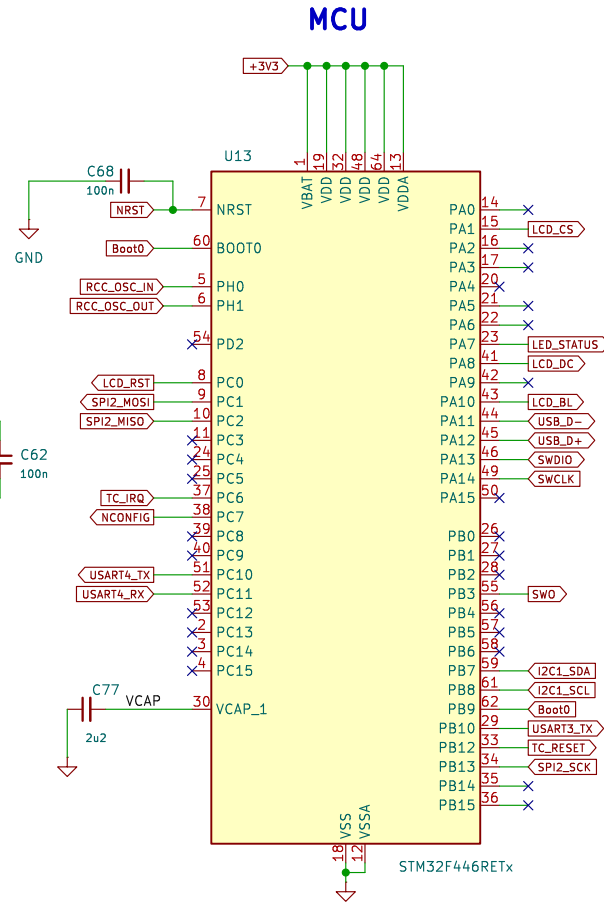
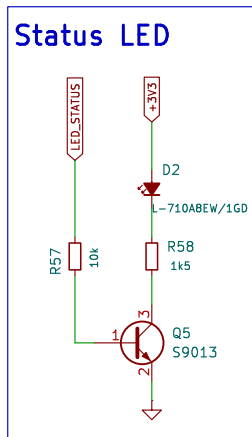
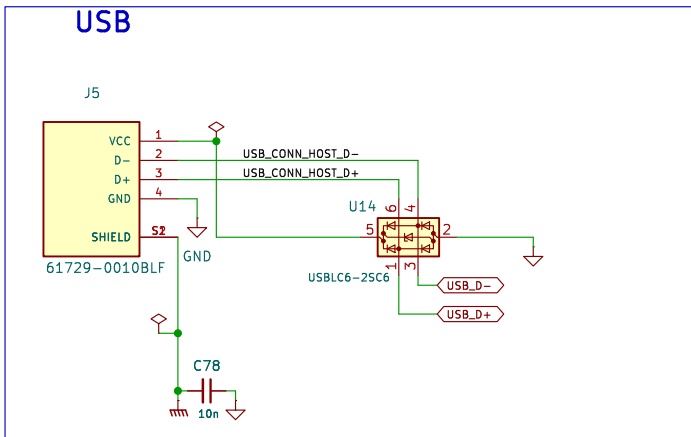
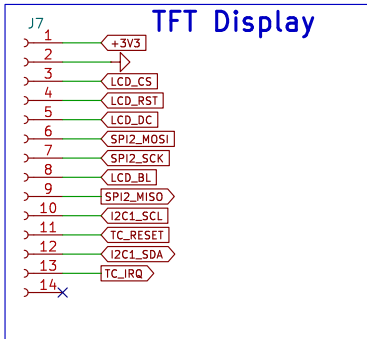
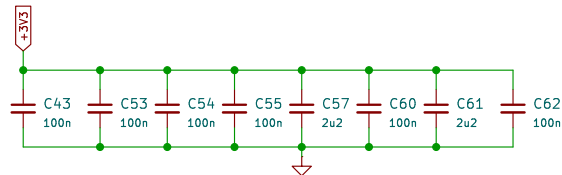
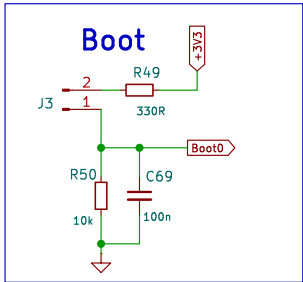
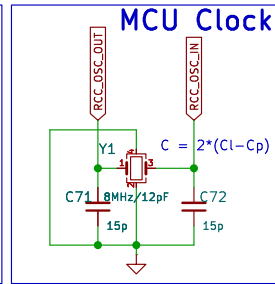
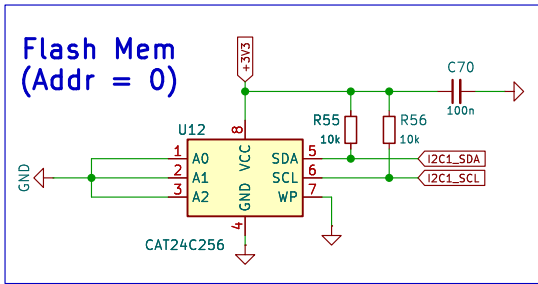


Ref Clock

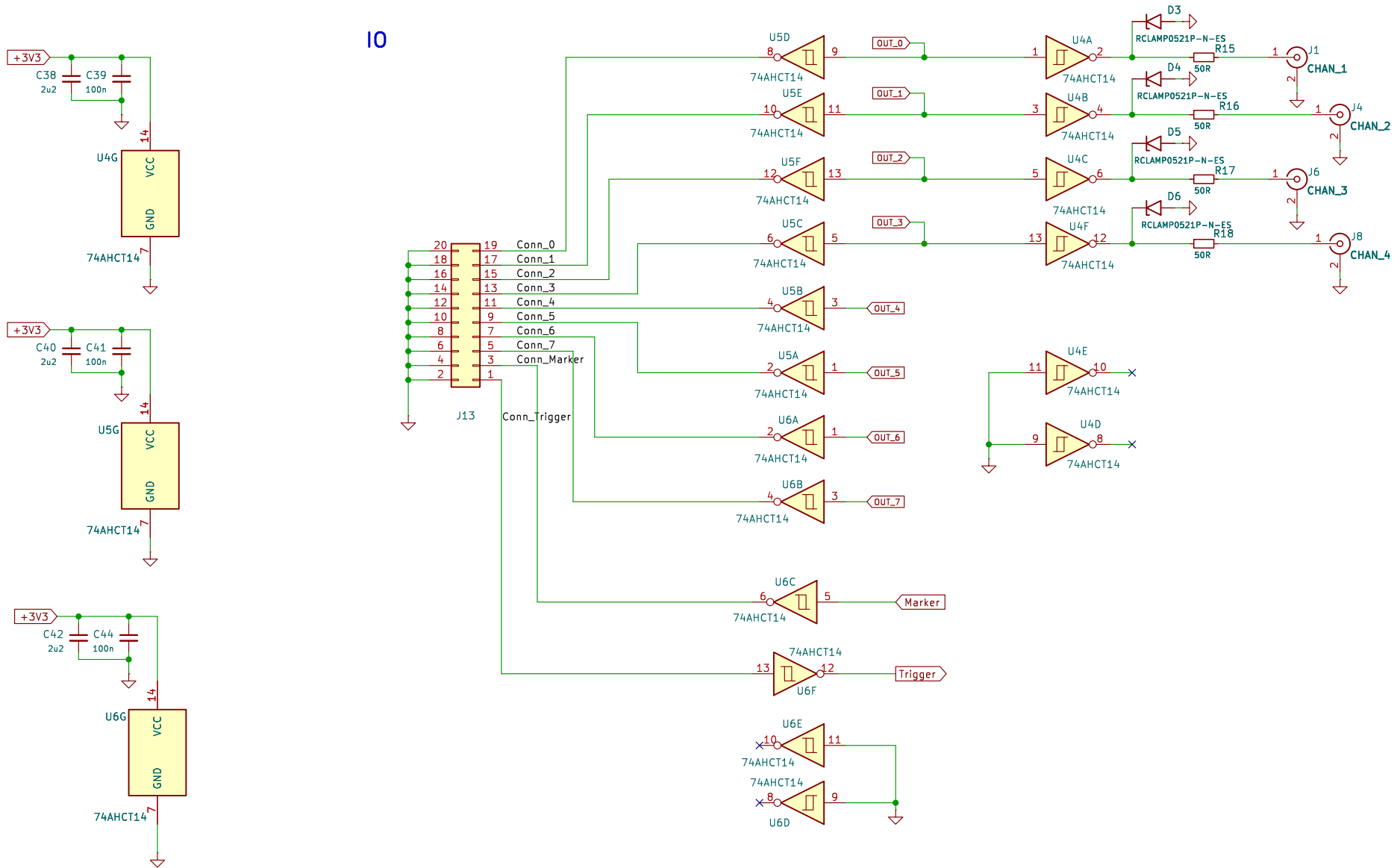


I/O





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FPGA Unused

